

FIG. 1

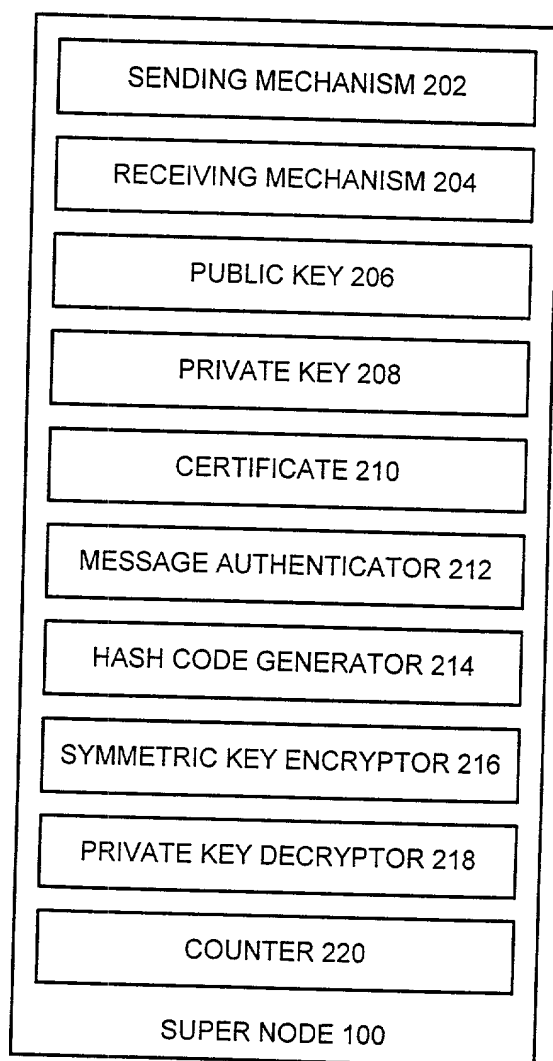


FIG. 2

TOP SECRET

A block diagram of Node 110, which is a vertical stack of 13 rectangular components. The components are labeled as follows from top to bottom: SENDING MECHANISM 302, RECEIVING MECHANISM 304, NODE KEY 306, MISSION KEY 308, MAC GENERATOR 310, PUBLIC KEY ENCRYPTOR 312, SYMMETRIC KEY ENCRYPTOR 314, SYMMETRIC KEY DECRYPTOR 316, NONCE GENERATOR 318, MAC VALIDATOR 320, HASH CODE GENERATOR 322, COUNTER 324, and CERTIFICATE 326. The entire stack is enclosed in a large rectangular border.

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graph TD; 302[SENDING MECHANISM 302] --- 304[RECEIVING MECHANISM 304]; 304 --- 306[NODE KEY 306]; 306 --- 308[MISSION KEY 308]; 308 --- 310[MAC GENERATOR 310]; 310 --- 312[PUBLIC KEY ENCRYPTOR 312]; 312 --- 314[SYMMETRIC KEY ENCRYPTOR 314]; 314 --- 316[SYMMETRIC KEY DECRYPTOR 316]; 316 --- 318[NONCE GENERATOR 318]; 318 --- 320[MAC VALIDATOR 320]; 320 --- 322[HASH CODE GENERATOR 322]; 322 --- 324[COUNTER 324]; 324 --- 326[CERTIFICATE 326];
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SENDING MECHANISM 302

RECEIVING MECHANISM 304

NODE KEY 306

MISSION KEY 308

MAC GENERATOR 310

PUBLIC KEY ENCRYPTOR 312

SYMMETRIC KEY ENCRYPTOR 314

SYMMETRIC KEY DECRYPTOR 316

NONCE GENERATOR 318

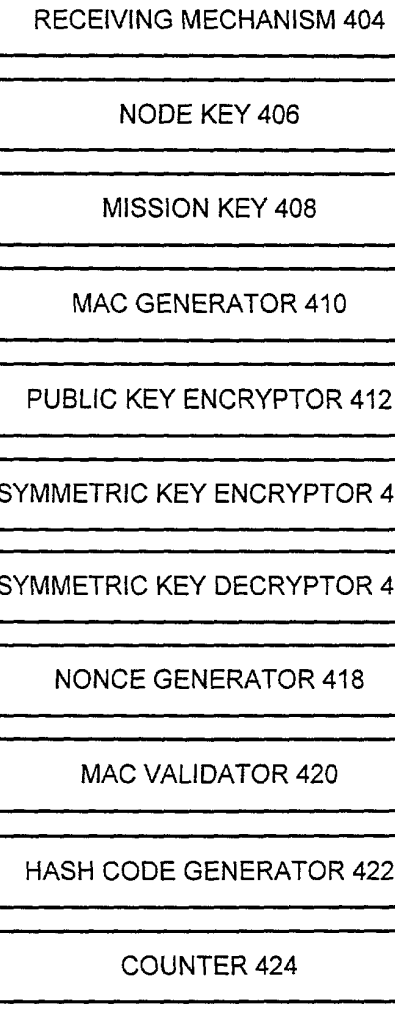
MAC VALIDATOR 320

HASH CODE GENERATOR 322

COUNTER 324

CERTIFICATE 326

NODE 110



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graph TD; A[SENDING MECHANISM 402] --- B[RECEIVING MECHANISM 404]; B --- C[NODE KEY 406]; C --- D[MISSION KEY 408]; D --- E[MAC GENERATOR 410]; E --- F[PUBLIC KEY ENCRYPTOR 412]; F --- G[SYMMETRIC KEY ENCRYPTOR 414]; G --- H[SYMMETRIC KEY DECRYPTOR 416]; H --- I[NONCE GENERATOR 418]; I --- J[MAC VALIDATOR 420]; J --- K[HASH CODE GENERATOR 422]; K --- L[COUNTER 424]; L --- M[CERTIFICATE 426]; M --- N[NODE 120];
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SENDING MECHANISM 402

RECEIVING MECHANISM 404

NODE KEY 406

MISSION KEY 408

MAC GENERATOR 410

PUBLIC KEY ENCRYPTOR 412

SYMMETRIC KEY ENCRYPTOR 414

SYMMETRIC KEY DECRYPTOR 416

NONCE GENERATOR 418

MAC VALIDATOR 420

HASH CODE GENERATOR 422

COUNTER 424

CERTIFICATE 426

NODE 120

FIG. 4

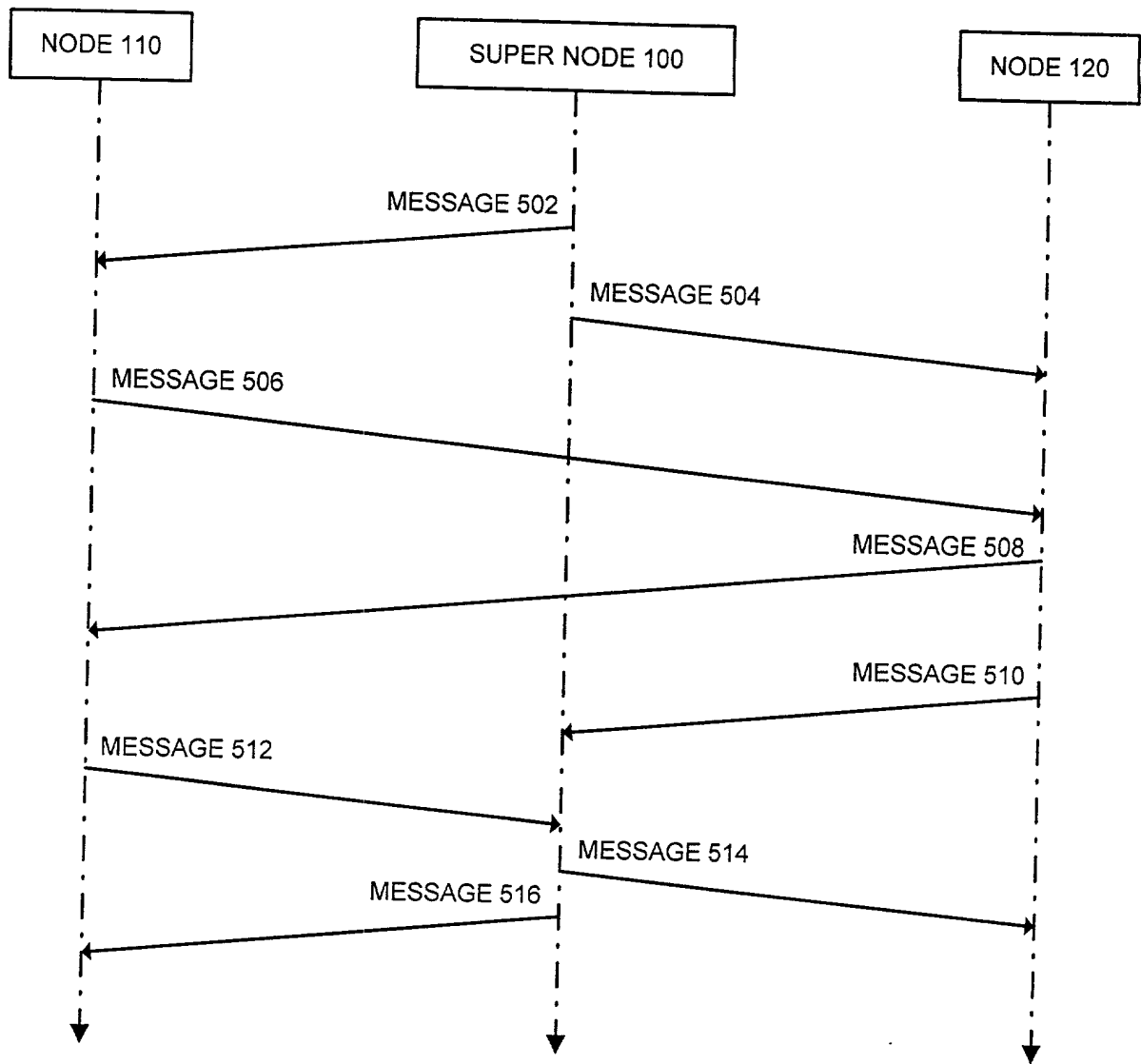


FIG. 5

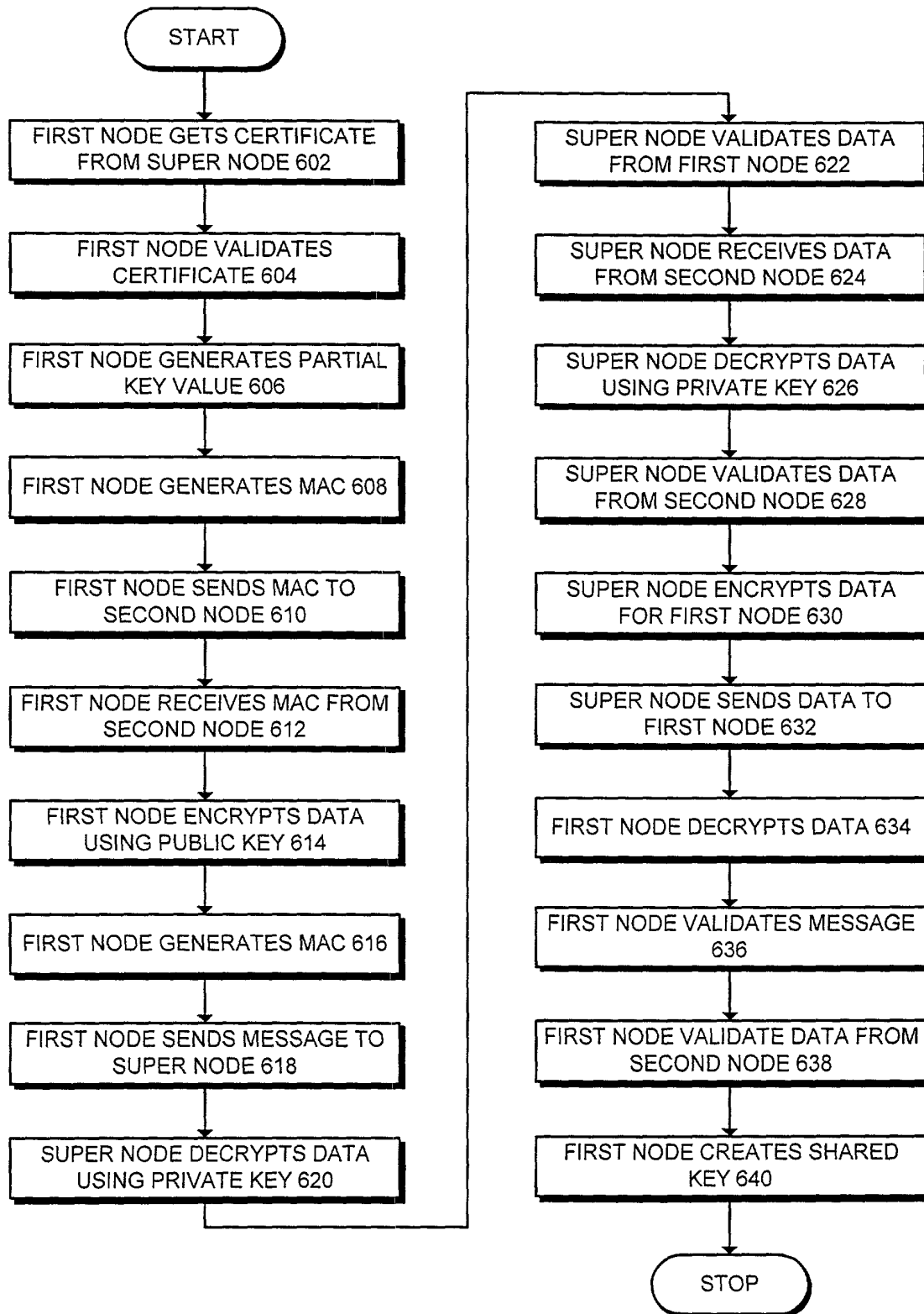


FIG. 6